

tools. The addition of a system-explorer window allows users to browse global fonts and background scripts; an enhanced properties window with hierarchical tree control gives a streamlined, application-dedicated parameter layout.

Base price: free. *DVT*, www.dvtsensors.com.

Odyssey Xpro+ features 1.4-GHz PowerPC

The Odyssey Xpro+ enhanced version of the Matrox Odyssey Xpro scalable vision-processor board features the latest version of the Freescale G4 PowerPC embedded microprocessor with a core frequency starting at 1.4 GHz, a Matrox-designed Oasis processing and router ASIC, and a customizable co-processor FPGA. The single-slot Odyssey Xpro+ also offers over 5 Gbytes/s of memory bandwidth, up to 2 GBytes of DDR SDRAM, up to 2 Gbytes/s of external I/O bandwidth, and PCI-X bus support.

Like the Odyssey Xpro, the Odyssey Xpro+ can capture from the majority of area- or line-scan video sources, thanks to a standard PCI mezzanine card (PMC) site located on the board that allows for the addition of Camera Link, analog, or LVDS/RS-422 frame-grabber modules. The Matrox Odyssey family offers support for Microsoft Windows 2000, Windows XP, and Linux.

Base price: \$6495. *Matrox Imaging*, www.matrox.com/imaging.

Tektronix debuts load-test system

The LTS21 load-test system for 3G cellular applications employs a patent-pending graphical user interface that lets users create complex, realistic 3G traffic scenarios using objects instead of scripts. Combining control-plane as well as user-plane traffic, it lets customers identify areas of weakness within a network and eliminate trouble spots through deployment of preventive measures.

The LTS21 targets technicians and engineers at network equipment manufacturers testing next-generation mobile data technologies as well as network operators who need

to evaluate Universal Mobile Telecommunication System (UMTS) or General Packet Radio Service (GPRS) network elements before making them operational within a live network. The LTS21 enables customers to test 3G networks with realistic but simulated voice calls and data streams from many thousands to many millions of mobile units.

Base price: \$250,000. *Tektronix*, www.tektronix.com.

Clock generator handles 2-GHz signals

Engineers often use RF synthesizers to generate clock signals over 1 GHz. With the CG635, you can free those expensive instruments for other tasks. The CG635 can generate single-ended and differential clocks at frequencies from 0.001 Hz



to 2.05 GHz. Jitter is less than 1 ps with phase noise less than -80 dB and with spurious response better than -70 dBc at 622.06 MHz, a common telecom data rate.

Output levels include CMOS, PECL, ECL, RS-485, and LVDS. Optional clock receiver modules (\$125) connect to the CG635 through a category-6 LAN cable. They let you produce outputs at distances up to 50 ft from the instrument without signal degradation. A PRBS option (\$550) provides you with pseudorandom data streams for testing high-speed serial bus components and systems.

Price: \$2490. *Stanford Research Systems*, www.thinksrs.com.

PCI Express analyzer handles ASI

Agilent Technologies had added support for PCI Express with Advanced Switching Interconnect (ASI) protocols to its E2980A family of protocol analyzers and exercisers. ASI adds features to PCI Express that enable it to operate as a backplane bus for "bladed" backplane-